i-manager's Journal on

Electrical Engineering (JEE)

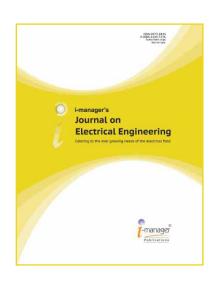
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- High level synthesis and hardware codesign
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- Processor Architectures
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- Electric Machines, Electric Vehicles,
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 Drives
- Energy Conversion and Management
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- Timing, Power, Thermal Analysis and Optimization
- Signal Integrity and Interconnect
- Design for Manufacturability
- Yield and Statistical Design
- Validation and Verification for Behavioral/Logic Design
- ♦ System-Level Synthesis and Optimization
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